

Appl. No.: 10/674,190
Amendment

Amendments to the Claims:

The listing of claims will replace all prior versions, and listings, of claims in the application.

Listing of Claims:

Claim 1 (currently amended): A method for automated testing of display signals from video graphics circuitry comprising:

capturing at least one display signal;

converting the display signal into at least one data acquisition signal; and

providing the at least one data acquisition signal to a test system that tests the display signal; and

taking time interval measurements of the at least one data acquisition signal.

Claim 2 (canceled).

Claim 3 (currently amended): The method of claim 1 wherein the data acquisition signals include at least one of the following: a vertical synchronization signal, a horizontal synchronization signal, a data enable signal, a pixel clock signal and a voltage control signal.

Claim 4 (original) The method of claim 1 wherein the display signals are also transmitted to the display device.

Claim 5 (currently amended): ~~The~~ A method of ~~claim 4~~ for automated testing of display signals from video graphics circuitry comprising:

capturing at least one display signal;

converting the display signal into at least one data acquisition signal; and

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providing the at least one data acquisition signal to a test system that tests the display signal, wherein the display signals are transmitted to the display device using at least one of low voltage differential signaling and transition minimized differential signaling.

Claim 6 (canceled):

Claim 7 (currently amended): The method of claim 4 wherein the display signals are transmitted to the display device using at least one of low voltage differential signaling, transition minimized differential signaling, and analog RGB signaling.

Claim 8 (original) The method of claim 1, wherein the display signals are generated by a computer under test and prior to capturing the display signals, the method further comprising:

providing at least one of the following: a keyboard command and a power change command, to the computer under test from a test computer to generate the display signals.

Claim 9 (currently amended): A method for automated testing of display information for a display device comprising:

providing a test command to a computer under test such that the computer under test generates display signals to be transmitted to the display device;

capturing the display signals to be received by the display device;

converting the display signals into at least one data acquisition signal; and

providing the at least one data acquisition signal to ~~a~~ the test system; and

taking time interval measurements of the at least one data acquisition signal using the test system.

Claim 10 (original) The method of claim 9 wherein prior to the step of providing the test command to the computer, the method includes:

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providing an original command to a command converter; and
generating the test command.

Claim 11 (currently amended): The method of claim 9 further comprising:

~~taking measurements of the at least one data acquisition signal with a test signal;~~
and generating a display accuracy report.

Claim 12 (currently amended) The method of claim 9 ~~11~~ wherein the step of taking time interval measurements of the at least one data acquisition signal includes:

measuring at least one of the following: a horizontal synchronization signal, a vertical synchronization signal, a data enable signal, a pixel clock signal, a voltage command signal and a backlight signal.

Claim 13 (original) The method of claim 9 wherein the display signal is at least one of the following: a low voltage differential signal, a transition minimized differential signal and an analog RGB signal.

Claim 14 (currently amended): An apparatus for automated testing of display signals from video graphics circuitry comprising:

a printed circuit board capable of receiving display signals;
a data acquisition signal generated by the printed circuit board from the display signals; ~~and~~
a test computer ~~that receives~~ configured to receive the data acquisition signal from the printed circuit board and tests to take time interval measurements of the display signals.

Claim 15 (currently amended): The apparatus of claim 14 further comprising:

a command generated by the test computer; and

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a command converter coupled to the test computer and the a computer under test such that the command converter receives the command from the test computer, generates a test command and provides the test command to the computer under test.

Claim 16 (original) The apparatus of claim 15 wherein the command converter generates at least one of the following: a keystroke command and a power change command.

Claim 17 (currently amended): ~~The~~ An apparatus ~~of claim 14~~ for automated testing of display signals from video graphics circuitry comprising:

a printed circuit board capable of receiving display signals;

a data acquisition signal generated by the printed circuit board from the display signals; and

a test computer configured to receive the data acquisition signal from the printed circuit board and to test the display signals, wherein the printed circuit board includes:

at least one line buffer; and

a low voltage differential signaling receiver coupled to the at least one buffer such that low voltage different signaling receiver generates the data acquisition signals that include at least one of the following: a vertical synchronization signal, a horizontal synchronization, a data enable signal and a pixel clock signal.

Claim 18 (currently amended): ~~The~~ An apparatus ~~of claim 14~~ for automated testing of display signals from video graphics circuitry comprising:

a printed circuit board capable of receiving display signals;

a data acquisition signal generated by the printed circuit board from the display signals; and

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a test computer configured to receive the data acquisition signal from the printed circuit board and to test the display signals, wherein the printed circuit board includes:

a transition minimized differential signaling bus;

a transition minimized differential signaling receiver coupled to the transition minimized differential signaling bus;

a transition minimized differential signaling transmitter coupled to the transition minimized differential signaling receiver across a signal bus; and

a plurality of buffers coupled to the signal bus for receiving at least one of the following signals being provided to the transition minimized differential signaling transmitter: a vertical synchronization signal, a horizontal synchronization, a data enable signal and a pixel clock signal.

Claim 19 (currently amended): An apparatus for automated testing of display signals from video graphics circuitry comprising:

a printed circuit board capable of receiving display signals;

a data acquisition signal generated by the printed circuit board from the display signal; and

a test computer operably coupled to the printed circuit board, the test computer including a processor operably coupled to a memory storing executable instructions such that the processor, in response to the executable instructions:

generates a command to be provided to a computer under test;

receiving receives the data acquisition signal; and

testing takes time interval measurements of the data acquisition signal.

Claim 20 (currently amended): The apparatus of claim 19 further comprising:

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a command converter operably coupled to the test computer, such that the command converter receives the command from the test computer and generates a test command to be provided to the a computer under test.